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## THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application: Michael E. Runas  
Serial No.: 08/315,934  
Filed: September 30, 1994  
Art Unit: 2511  
Examiner: A. Zarabian  
For: CIRCUITS, SYSTEMS AND METHODS FOR  
IMPROVING PAGE ACCESSES AND BLOCK  
TRANSFERS IN A MEMORY SYSTEM

4/a  
jl  
8-18-95

Honorable Commissioner of  
Patents and Trademarks  
Washington, D.C. 20231

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GROUP 2000

Dear Sir:

In response to the Office Action mailed March 23, 1995, Applicant respectfully requests that the Examiner reconsider the rejections and objections to the claims in view of the following amendments and comments. Please amend the application as follows.

In the Claims

Please amend Claims 1, 2, 7, 14, 15, and 22-24 as follows.

1. (Amended) A memory comprising:

a)

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